

# REGISTRATION FORM

## STUDENT ATTENDEE REGISTRATION FORM (ICSE2014)

Item	Participant Name (Please print clearly or attach business card)	Position
1.		
2.		
3.		
Industry Sector :		
Company :		
Address :		
		Postcode :
*Primary Person :		*Mobile Phone :
*Tel No. :	*Fax No. :	*Email :

\* Please tick the events that you would like to attend:  
-includes morning and evening tea breaks, lunch and conference program book

Walk-in Registration (Student)					
TYPE			International Credit Card or Wire Transfer (USD)		Please Tick (✓)
			Local Participants or CDM or Cheque (RM)		
Registration Student IEEE Member	Conference Dinner	27-Aug-14	75	240	
		28-Aug-14	75	240	
		29-Aug-14	50	160	
		27-Aug-14	63	200	
Registration Student Non-IEEE Member	Conference Dinner	27-Aug-14	94	300	
		28-Aug-14	94	300	
		29-Aug-14	63	200	
		27-Aug-14	78	250	
			<b>TOTAL</b>		

### Mode of Payment

I enclose  Crossed Cheque  Bank Draft  Money Order  LO/PO

Number	Bank	No. of Participants:	
Total Sum : RM			
Payment must be made payable to <b>ICSE</b>			

Bank Transfer [Please email your Bank-in Slip]  
Pay Name: ICSE  
Name of Bank: CIMB Bank Berhad  
Account Number: **8002167768**



# ICSE 2014

## 11<sup>th</sup> IEEE INTERNATIONAL CONFERENCE ON SEMICONDUCTOR ELECTRONICS

27<sup>th</sup> – 29<sup>th</sup> August 2014

Berjaya Times Square Hotel,  
Kuala Lumpur, Malaysia

### Secretariat of IEEE-ICSE2014

Electron Devices Malaysia Chapter  
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<http://ieeemalaysia-eds.org/icse2014/home.html>

Organized by: ELECTRON DEVICES SOCIETY



Co-organized by:



# Tentative Agenda

## Day 1: August 27, 2014 (Wednesday)

0800 - 0850	Conference Registration
0850 - 0910	Welcoming Address by EDS Chair
0910 - 1000	Session 1 (A,B,C)
1000 - 1015	Coffee Break
1015 - 1100	Session 1 (A,B,C) continued
1100 - 1130	<b>Keynote Paper I:</b> Convergence of Emerging Technologies to Address the Challenges of the 21st Century by <b>Distinguished Prof. Dr. Asad M. Madni</b>
1130 - 1200	<b>Keynote Paper II:</b> There is Plenty of Room at the Silicon by <b>Prof. Dr. Park Young June</b>
1200 - 1230	<b>Keynote Paper III:</b> Realization of GaN-Based Technology for High Power and High Power Applications by <b>Prof. Dr. Edward Yi Chang</b>
1230 - 1245	Photo Session
1245 - 1400	Lunch Break
1400 - 1605	Session 2 (A,B,C)
1605 - 1615	Coffee Break
1615 - 1700	Session 2 (A,B,C) continued
1700 - 1730	Poster Session I
1930 - 2200	Opening Ceremony and Conference Dinner

## Day 2: August 28, 2014 (Thursday)

0900 - 1000	Session 3 (A,B,C)
1000 - 1015	Coffee Break
1015 - 1115	Session 3 (A, B,C) continued
1115 - 1145	<b>Keynote Paper IV:</b> Amorphous Oxide Electronics by <b>Prof. Dr. Arokia Nathan</b>
1145 - 1215	<b>Keynote Paper V:</b> Acoustic Metamaterials & Phononic Crystals: Towards the Total Control of the Wave Propagation by <b>Prof. Dr. Abdelkrim Khelif</b>
1215 - 1245	<b>Keynote Paper VI:</b> SOI Photodiode with Surface Plasmon Antenna: From Sensitivity Enhancement to Refractive Index Measurement for Biosensing by <b>Prof. Dr. Hiroshi Inokawa</b>
1245 - 1400	Lunch Break
1400 - 1600	Session 4 (A, B, C)
1600 - 1615	Coffee Break
1615 - 1700	Session 4 (A, B, C) continued
1700 - 1730	Poster Session II

## Day 3: August 29, 2014 (Friday)

0900 - 1000	Session 5 (A,B,C)
1000 - 1015	Coffee Break
1015 - 1100	Session 5 (A, B,C) continued
1100 - 1200	Award Presentation and Closing Ceremony
1200 - 1430	Lunch Break

## Organizing Committee

Chair:	Prof. Dato' Dr. Burhanuddin Yeop Majlis, IMEN UKM
Co-Chair:	Assoc. Prof. Dr. Mohd Nizar Hamidon, UPM
Hon. Secretary:	Assoc. Prof. Dr. P. Sushitha Menon, IMEN UKM
Treasurer:	Assoc. Prof. Dr. Roslina Mohd. Sidek, UPM



### Distinguished Prof. Dr. Asad M. Madni

President, Chief Operating Officer & CTO (Emeritus) of BEI Technologies Inc., Distinguished Adjunct Professor/Distinguished Scientist, Electrical Engineering Department, University of California Los Angeles, USA

**Title:** Convergence of Emerging Technologies to Address the Challenges of the 21<sup>st</sup> Century

Prof. Dr. Asad Madni served as President, COO & CTO of BEI Technologies Inc. from 1992 until his retirement in 2006. He led the development & commercialization of intelligent micro-sensors, systems, and instrumentation for which he has received worldwide acclaim. Prior to BEI he was with Sysron Donner Corporation for 18 years in senior technical & executive positions, eventually as Chairman, President & CEO. Here, he made seminal and pioneering contributions in the development of RF & Microwave Systems & Instrumentation which significantly enhanced the capabilities of the US Tri-Services. He is currently a Distinguished Adjunct Professor/Distinguished Scientist at UCLA, Distinguished Professor at TCI College of Technology, Adjunct Professor at Ryerson University and Executive Managing Director & CTO of Crocker Capital. He received an A.A.S. from RCA Institutes Inc., B.S. & M.S. from UCLA, Ph.D. from California Coast University, D.Sc. (H) from Ryerson University, D.Eng. (H) from Technical University of Crete and Sc.D. (H) from California State University/CSUN. He is also a graduate of the Engineering Management Program at Caltech, the Executive Institute at Stanford, and the Program for Senior Executives at MIT Sloan School of Management. He is credited with over 150 refereed publications, 68 issued or pending patents and is the recipient of numerous national and international honors and awards including election to the US National Academy of Engineering. He is a Fellow/ Eminent Engineer of 14 of the world's most prestigious professional academies and societies.



### Prof. Dr. Arokia Nathan

Professor and Chair, Photonic System and Displays Electrical Engineering Division, Cambridge University, UK

**Title:** Amorphous Oxide Electronics

Prof. Dr. Arokia Nathan holds the Chair of Photonic Systems and Displays in the Department of Engineering, Cambridge University. He received his PhD in Electrical Engineering from the University of Alberta. Following post-doctoral years at LSI Logic Corp., USA and ETH Zurich, Switzerland, he joined the University of Waterloo where he held the DALSA/NSERC Industrial Research Chair in sensor technology and subsequently the Canada Research Chair in nano-scale flexible circuits. He was a recipient of the 2001 NSERC E.W.R. Steacie Fellowship. In 2006, he moved to the UK to take up the Sumitomo Chair of Nanotechnology at the London Centre for Nanotechnology, University College London, where he received the Royal Society Wolfson Research Merit Award. He has held Visiting Professor appointments at the Physical Electronics Laboratory, ETH Zurich and the Engineering Department, Cambridge University, UK. He has published over 400 papers in the field of sensor technology and CAD, and thin film transistor electronics, and is a co-author of four books. He has over 50 patents filed/awarded and has founded/co-founded four spin-off companies. He serves on technical committees and editorial boards in various capacities. He is a Chartered Engineer (UK), Fellow of the Institution of Engineering and Technology (UK), Fellow of IEEE (USA) and an IEEE/EDS Distinguished Lecturer.



### Prof. Dr. Edward Yi Chang

Dean of Research and Development & Chair Professor National Chiao Tung University, Taiwan R.O.C

**Title:** Realization of GaN-based Technology for Future High Power & High Frequency Applications

Prof. Dr. Edward Chang received his B.S. degree in Materials Science and Engineering from National Tsing Hua University, Hsinchu, Taiwan in 1977, and his Ph.D. degree in Materials Science and Engineering from University of Minnesota, Minneapolis, USA in 1985. Prof. Chang is currently the Dean of Research and Development and professor of the Dept. of Materials Science and Engineering and Dept. of Electrical Engineering at National Chiao Tung University. He is also the director of Diamond Lab and the director of NCTU-TSMC research center. Prof. Chang is a senior member and a Distinguished Lecturer of the IEEE Electron Devices Society. Currently, his research activities include InP, GaAs based compound materials and devices (HEMT, HBT) for wireless communication and sub-millimeter wave imaging applications, GaN based materials (MBE, MOCVD) and high frequency & high power electronic (HEMT) applications. He has received quite a few honors from Taiwan and abroad, including two times Outstanding Research Award and Distinguished Contribution for Technical Transfer to Industry, both from National Science Council, Taiwan.



### Prof. Dr. Young June Park

Professor, Physical Electronics Laboratory, School of Electrical Engineering, Seoul National University, Korea

**Title:** There is Plenty of Room at the Silicon

Prof. Dr. Young June Park received the B.S. and M.S. degrees in Electrical Engineering from Seoul National University, in 1975 and 1977, respectively. He received the Ph.D. degree in Electrical Engineering from University of Massachusetts, USA, in 1983. From 1983 to 1988, he worked for IBM, East Fishkill, NY and LG Semiconductor, Seoul, as a research staff member. In 1988, he joined Seoul National University as a faculty member and has contributed to education, semiconductor lab. establishment, consulting to companies (as the R&D director in SKhynix) and government of Korea. His research areas of interest include nano semiconductor device physics, reliability and bio molecular sensing using semiconductor devices.



### Prof. Dr. Hiroshi Inokawa

Professor, Research Institute of Electronics Shizuoka University, Japan

**Title:** SOI Photodiode with Surface Plasmon Antenna: from Sensitivity Enhancement to Refractive Index Measurement for Biosensing

Prof. Dr. Hiroshi Inokawa received his Ph.D. degree in Electrical Engineering from Kyoto University, Japan in 1985. In the same year, he joined the Atsugi Electrical Communications Laboratories, Nippon Telegraph and Telephone Corporation (NTT), Kanagawa, Japan. Since then, he has been engaged in the research and development of scaled-down CMOS and silicon single-electron devices. During the course of his research, he invented the basic structure of FinFET in 1989 and single-electron multiple-valued logic in 2001 and received IEEE International Symposium on Multiple-Valued Logic (ISMVL) Outstanding Contributed Paper Awards in 2004 and 2006, Director's Award of NTT Basic Research Laboratories in 2004 and 28th JSAP Award for the Best Original Paper in 2006. In 2006, he became a professor of the Research Institute of Electronics, Shizuoka University, Hamamatsu, Japan, where he has been studying nanodevices for advanced circuits and systems. His recent work on SOI MOSFET single-photon detector was introduced by IEEE Photonics Journal in 2012 as a Breakthrough in Photonics. Prof. Inokawa is a member of the Institute of Electrical and Electronics Engineers (IEEE), the Japan Society of Applied Physics (JSAP), the Institute of Electronics, Information and Communication Engineers of Japan (IEICE), and the Institute of Electrical Engineers of Japan (IEEJ). He has served as a JSAP board member of representative in 2001-2003, an editor of JJAP in 2007-2013, the chair of the IEEE survey committee of silicon nanosystem integration technology in 2009-2011 and an advisory committee member of NICT Japan Trust International Research Cooperation Program in 2006-2009. Prof. Inokawa is also a researcher of National Institute of Science and Technology Policy (NISTEP) since 2002.



### Prof. Dr. Abdelkrim Khelif

Professor and Senior Researcher CNRS, French National Centre for Scientific Research (FEMTO-ST), France

**Title:** Acoustic Metamaterials and Phononic Crystals: Towards the Total Control of the Wave Propagation

Prof. Dr. Abdelkrim Khelif is a senior researcher at CNRS (Centre National de la Recherche Scientifique). He has extensive experience in design, optimization and simulation of phononic structures. He contributes a lot in the experimental demonstration of ultrasonic phononic crystal band gaps, cavities made and waveguide at low frequency. He is also active in extracting the potential application of phononic crystal as a new acoustic device for signal processing and sensing applications. He has multiple informal collaborations in the world. Due to his achievements in the field of phononic crystals, Prof. Khelif has been involved as the symposium session chair, technical committee member and invited speaker in several conferences for the last 5 years. He is also a frequent reviewer for various physics and acoustic journals such as Physical Review Letter, B, E, Applied Physics Letters, and Journal of Applied Physics. He has more than 60 journal papers and several invited, and contributed conference and seminar presentations. He also has 1 patent in phononic crystal and the recipient of Bronze medal awards from CNRS for his work in phononic crystals.

